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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/638,401	08/12/2003	Hirofumi Kawai	241422US2	5469
22850 7.	590 01/28/2005		EXAMINER	
OBLON, SPIVAK, MCCLELLAND, MAIER & NEUSTADT, P.C. 1940 DUKE STREET			KENNEDY, JENNIFER M	
ALEXANDRIA, VA 22314			ART UNIT	PAPER NUMBER
			2812	-
			DATE MAILED: 01/28/200	5

Please find below and/or attached an Office communication concerning this application or proceeding.

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	Application No.	Applicant(s)	af Li				
	10/638,401	KAWAI, HIROFUMI					
Office Action Summary	Examiner	Art Unit					
	Jennifer M. Kennedy	2812					
The MAILING DATE of this communication a Period for Reply	appears on the cover sheet w	ith the correspondence address	_				
A SHORTENED STATUTORY PERIOD FOR REF THE MAILING DATE OF THIS COMMUNICATION - Extensions of time may be available under the provisions of 37 CFR after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a r - If NO period for reply is specified above, the maximum statutory perion - Failure to reply within the set or extended period for reply will, by state Any reply received by the Office later than three months after the main earned patent term adjustment. See 37 CFR 1.704(b).	N. 1.136(a). In no event, however, may a reply within the statutory minimum of thi od will apply and will expire SIX (6) MOI tute, cause the application to become A	reply be timely filed ty (30) days will be considered timely. NTHS from the mailing date of this communication. BANDONED (35 U.S.C. § 133).					
Status							
1) Responsive to communication(s) filed on 10	November 2004.						
	his action is non-final.						
3) Since this application is in condition for allow	vance except for formal mat	ters, prosecution as to the merits is					
closed in accordance with the practice under	r <i>Ex parte Quayle</i> , 1935 C.[D. 11, 453 O.G. 213.					
Disposition of Claims							
4) Claim(s) 6-10 and 20-22 is/are pending in th	e application.						
4a) Of the above claim(s) 7-10 is/are withdra	wn from consideration.						
5) Claim(s) is/are allowed.	•						
6)⊠ Claim(s) <u>6 and 20-22</u> is/are rejected.	Claim(s) <u>6 and 20-22</u> is/are rejected.						
<u> </u>	Claim(s) is/are objected to.						
8) Claim(s) are subject to restriction and	l/or election requirement.						
Application Papers							
9) The specification is objected to by the Exami							
10) The drawing(s) filed on is/are: a) □ a	· · · · · · · · · · · · · · · · · · ·						
Applicant may not request that any objection to the		• •					
Replacement drawing sheet(s) including the corre		• •					
11) The oath or declaration is objected to by the	Examiner. Note the attache	Office Action or form P1O-152.					
Priority under 35 U.S.C. § 119							
12)⊠ Acknowledgment is made of a claim for foreig a)⊠ All b)□ Some * c)□ None of:	gn priority under 35 U.S.C.	§ 119(a)-(d) or (f).					
1. Certified copies of the priority docume	nts have been received.						
2. Certified copies of the priority docume	nts have been received in A	pplication No					
3. Copies of the certified copies of the pr	iority documents have been	received in this National Stage					
application from the International Bure	` ` ' ' '						
* See the attached detailed Office action for a list	st of the certified copies not	received.					
	•						

Attachment(s)

2) Notice of Draftsperson's Patent Drawing Review (PTO-948)

3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date 10/28/2004.

4)	Ш	Interview Summary (PTO-413))
		Paper No(s)/Mail Date.	

5) Notice of Informal Patent Application (PTO-152)
6) Other:

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DETAILED ACTION

Response to Amendment

In view of Applicant's amendment to the specification, the objection is withdrawn.

In view of Applicant's cancellation of the claims 11-16, the objections to the claims are rendered moot.

Drawings

The drawings were received on November 10, 2004. These drawings are acceptable.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claim 6, and 20-22 is rejected under 35 U.S.C. 103(a) as being unpatentable over Ryun et al. (U.S. Patent No. 5,484,737) in view of Seki (U.S. Patent No. 5,032,888).

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Ryun et al. disclose a collector layer comprising a first kind of semiconductor material (31);

a base layer including a first base portion (Si of Si/SiGe or Si/SiGe/Si layer) and a second base portion (32, see column 4, lines 60-62), said first base portion coming in contact with the first collector layer and comprising the first kind of semiconductor material, said second base portion (SiGe of Si/SiGe or Si/SiGe/Si layer) coming in contact with the first base portion and comprising a second kind of semiconductor material; and

an emitter layer (34) coming in contact with the base layer and comprising the first kind of semiconductor material, said emitter layer forming a heterojunction with the base layer.

Ryun et al. but do not disclose the device wherein a film thickness of the first base portion is set such that, when a bipolar transistor having the base layer, the emitter layer, and the collector layer is in a non-saturated operation state, a depletion layer extending form a junction between the collector layer and the base layer does not reach the second base layer.

The examiner notes that Seki et al. discloses that base layer thickness is chosen so as to set the breakdown voltage of the device (see column 4, lines 15-28).

The examiner notes that Applicant does not teach that the base layer thickness solves any stated problem or is for any particular purpose other than that of preventing the lowering of the breakdown voltage. Thus, it would have been obvious to one of

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ordinary skill in the art at the time the invention was made to have a film thickness of the first base portion be set such that, when a bipolar transistor having the base layer, the emitter layer, and the collector layer is in a non-saturated operation state, a depletion layer extending from a junction between the collector layer and the base layer does not reach the second base layer so as to set the breakdown voltage as Seki et al. teaches, and because it has been held that where the general conditions of a claim are disclosed in the prior art, discovering the optimum or workable ranges involves only routine skill in the art. *In re Aller*, 105 USPQ 233, MPEP 2144.05 II A.

The examiner notes that a preamble is generally not accorded any patentable weight where it merely recites the purpose of a process or the intended use of a structure, and where the body of the claim does not depend on the preamble for completeness but, instead, the process steps or structural limitations are able to stand alone. See *In re Hirao*, 535 F.2d 67, 190 USPQ 15 (CCPA 1976) and *Kropa v. Robie*, 187 F.2d 150, 152, 88 USPQ 478, 481 (CCPA 1951).

In re claim 20, Ryun et al. discloses the device wherein an energy gap of the first kind of semiconductor material is larger than that of the second kind of semiconductor material (energy gap of silicon is 1.1 eV, energy gap of germanium is 0.67 eV, therefore the energy gap of the first kind (Si) is larger than that of the second kind (SiGe)).

In re claim 21, Ryun et al. discloses the device wherein a breakdown field of the first kind of semiconductor material is larger than that of the second kind of semiconductor material. (breakdown field of silicon is 30 V/µm, breakdown field of

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germanium is 8 V/µm, therefore breakdown field of the first kind (Si) is larger than that of the second kind (SiGe)).

In re claim 22, Ryun et al. discloses the device wherein the first kind of semiconductor material is silicon, and the second kind of semiconductor material is silicon germanium (see column 4, lines 60-61).

Response to Arguments

Applicant's arguments filed November 10, 2004 have been fully considered but they are not persuasive.

Applicant's argue that Seki does not disclose a BiCMOS. In response to applicant's arguments, the recitation of "a BiCMOS device operated with an RF signal" has not been given patentable weight because the recitation occurs in the preamble. A preamble is generally not accorded any patentable weight where it merely recites the purpose of a process or the intended use of a structure, and where the body of the claim does not depend on the preamble for completeness but, instead, the process steps or structural limitations are able to stand alone. See *In re Hirao*, 535 F.2d 67, 190 USPQ 15 (CCPA 1976) and *Kropa v. Robie*, 187 F.2d 150, 152, 88 USPQ 478, 481 (CCPA 1951).

Further, Applicant's argue that the device of Seki is much different from that of the claimed device. Applicant notes that the thickness of the base layer of an IGBT is different from that of a BiCMOS. Again, it is noted that in independent claim 6, no

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patentable weight has been given to the limitations of a BiCMOS. The examiner notes that a BiCMOS is simply defined as a bipolar transistor and MOS transistors on the same chip and that no specific thicknesses of the base layer are associated with a BiCMOS. Further, it is noted that no specific thickness of layers are claimed, other than that relative to the depletion layer. The examiner notes that the device of Seki is an IGBT, which is a bipolar transistor. Seki is combined with Ryun et al., a bipolar transistor. Seki is only relied upon to show that the base layer's thickness is determined to set the breakdown voltage of the device (see column 4, lines 15-28). Determination of the base layer's thickness based on the breakdown voltage of the device is a concept applicable to all bipolar transistors.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jennifer M. Kennedy whose telephone number is (571) 272-1672. The examiner can normally be reached on Mon.-Fri. 9:30-6:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael S. Lebentritt can be reached on (571) 272-1873. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Jennifer M. Kennedy
Patent Examiner
Art Unit 2812

jmk